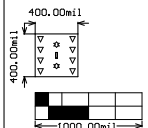
Layer	Name	Material	Thickness	Constant	Board Layer Stack
1	Top Overlay				
2	Top Solder	Solder Resist	1.00mil	3.5	
3	Top Layer	Copper	0.91mil		
4	Dielectric1	Polyimide	1.00mil	4.8	
5	Bottom Layer	Copper	0.91mil		
6	Bottom Solder	Solder Resist	1.00mil	3.5	
7	Bottom Overlay				

Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
✡	2	49.21mil (1.250mm)	NPTH	Round	Top Layer - Bottom Layer	
▽	8	23.00mil (0.584mm)	PTH	Round	Top Layer - Bottom Layer	
	10 Total					



DESIGN INFORMATION

MIN. TRACK WIDTH: 9.8 MIL
MIN. CLEARANCE: 3.9 MIL
MIN. VIA PAD SIZE: 24 MIL
MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL
PER IPC-D-275 CLASS 2 LEVEL C
REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL
HOLE SIZE TOLERANCE (UNLESS OTHERWISE SPECIFIED): +/- 3 MIL

MATERIAL:
[ ] FR-408 [ ] FR-4 High Tg [X] OTHER POLYIMIDE
THICKNESS: [ ] 62 MIL (1.8mm) +/-10% [X] OTHER 5.1 +/-1.9 MIL
TOLERANCE: [X] ANSI IPC-6012 TYPE 3 CLASS 2
[ ] OTHER +/- _____
BOW & TWIST: [X] ANSI IPC-6012 TYPE 3 CLASS 2
[ ] OTHER +/- _____

DRILLING:
REFERENCE: [X] AS SHOWN [X] NC_DRILL FILES
PTH COPPER THICKNESS: [X] 20-30 um [ ] OTHER _____

BOARD FINISH:
SILKSCREEN: [ ] TOP [ ] BOTTOM
SILKSCREEN COLOR: [ ] WHITE [ ] OTHER _____
SOLDER RESIST COLOR: [ ] GREEN [X] OTHER BLACK
[X] MATTE [ ] SEMI-GLOSS

SURFACE FINISH: [X] IMMERSION GOLD (ENIG) [ ] ENEPIG
[ ] IMM. TIN/SILVER OR EQUIV [ ] OTHER _____

ARRAY/PANEL: [ ] CUT AND TRIM PER M1 BOARD OUTLINE
[ ] N.C. ROUTE [X] V. SCORE

CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBS TO MEET OR EXCEED THE REQUIREMENTS OF:
[X] ANSI IPC-A-600F CLASS -> [ ] 1 [X] 2 [ ] 3
[X] RoHS [ ] OTHER PER ORDER

ALL BOARDS MUST MEET OR EXCEED UL94-V0 REQUIREMENTS.
PCB MUST BEAR THE UL94V-0 UL REGISTERED MATERIAL ID NUMBER

ADDITIONAL REQUIREMENTS:
MICROSECTION: [ ] YES
BARE BOARD ELEC. TEST: [ ] NONE [X] REQUIRED [ ] PER ORDER

TEXAS INSTRUMENTS

PROJECT TITLE: MHR046A
DESIGNED FOR: Public Release
FILE NAME: MHR046A_PCB.PcbDoc

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: MHR046	REV: A	SVN REV: Not In VersionControl
LAYER NAME = [illegible]ions	TID #: N/A		
PLOT NAME = Fabrication Drawing	GENERATED : 10/12/2016 3:42:19 PM	TEXAS INSTRUMENTS	

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ENGINEER: James Becker	LAYOUT BY: Krypton Solutions-BA
SCALE: 0.56	ALTIUM DESIGNER VERSION: 16.1.9.221